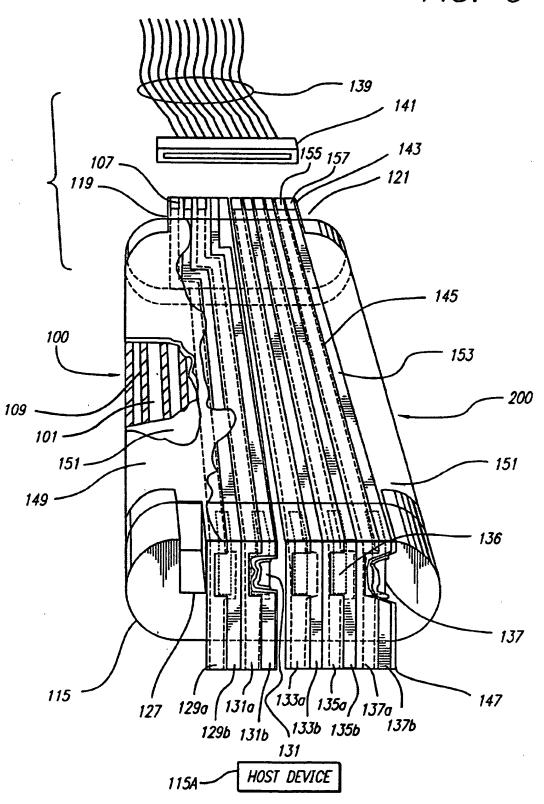
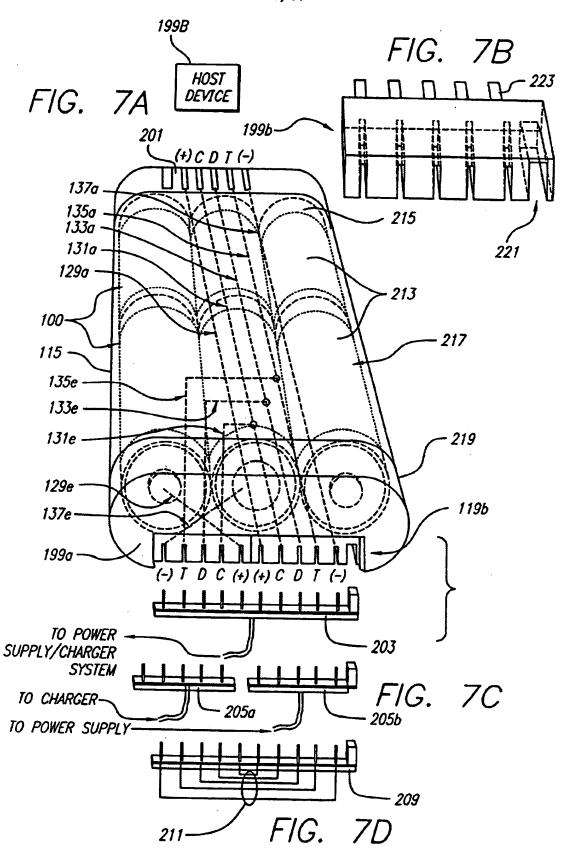
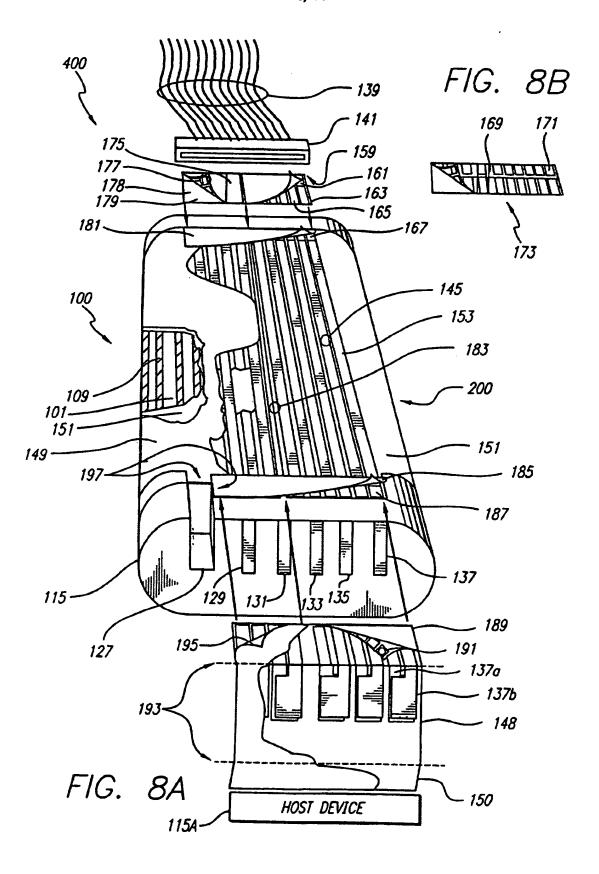
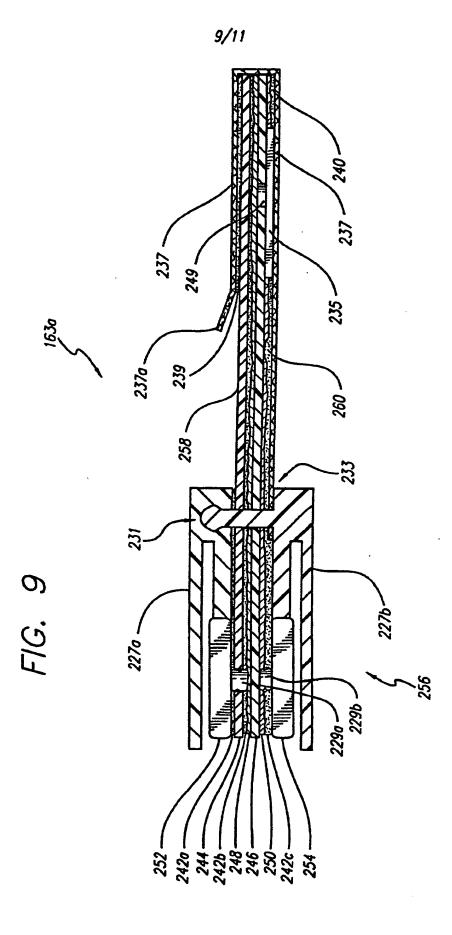


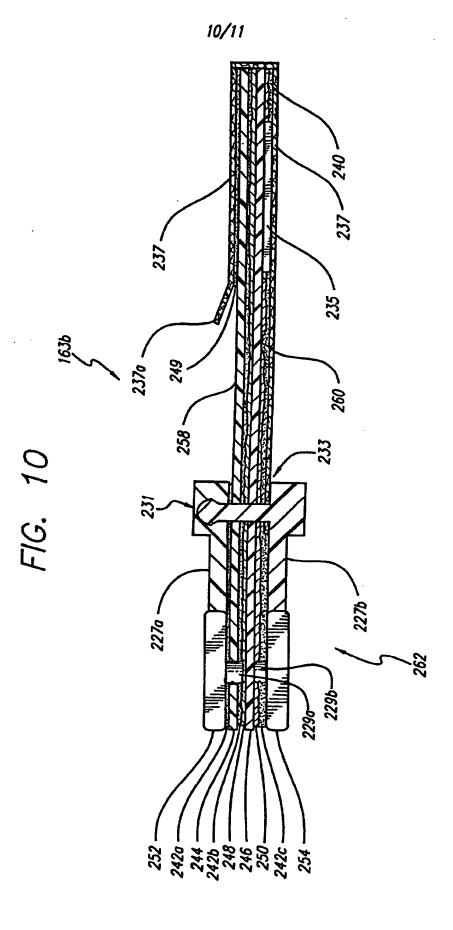
FIG. 6











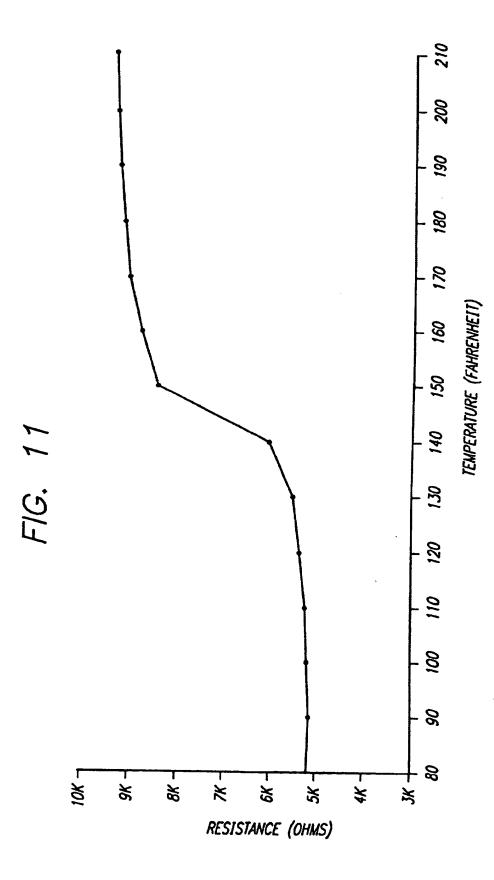
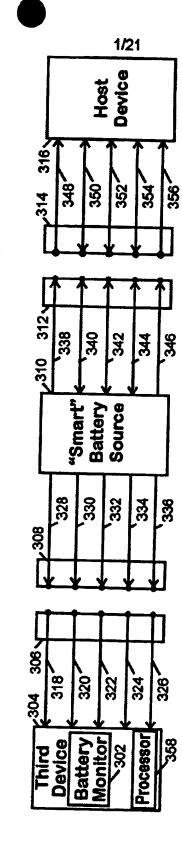


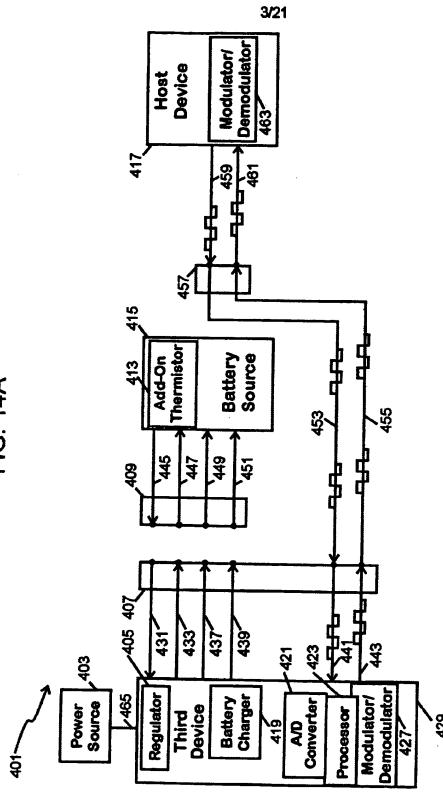
FIG. 12





2*1*21 Device External Data I/O Port Regulator **J** USB 3860 399 370B 383 381 393 375 361 r376 Battery Add-On Thermistor Source 371 391 369 365 398 83 367 7374 ,362 372<sub>V</sub> 382 386 ब्रुह 392 388 390 386 397 389 Converter Voltage Regulator Third Device A/D Converter Processor USB Comm Circuit ₽ 366 88

FIG. 13



477 Host Device USB M/D /463B Data 1/0 Port Regulator 405B 7 417A **489** 463A **4**61 477A 7405A Modulator/ Demodulator 463 USB \$ Thermistor **Battery** Source Adapter Add-On ₩, **4** 445 4 451 455 405 407 431 433 437 439 423 <u>\$</u> Battery Charger Demodulator A/D Converter 465 Regulator Modulator/ Source Third Device Power Processor 429 427 462

FIG. 14B

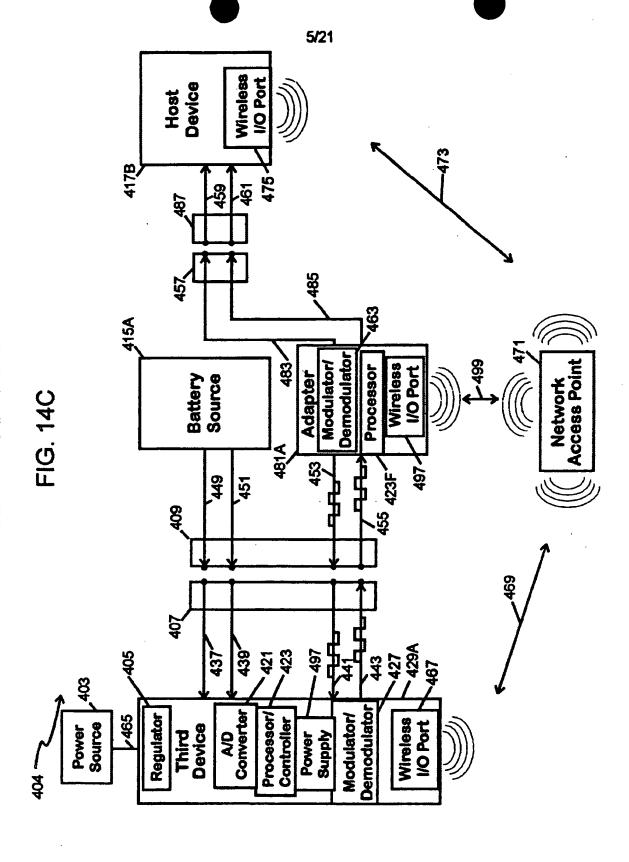
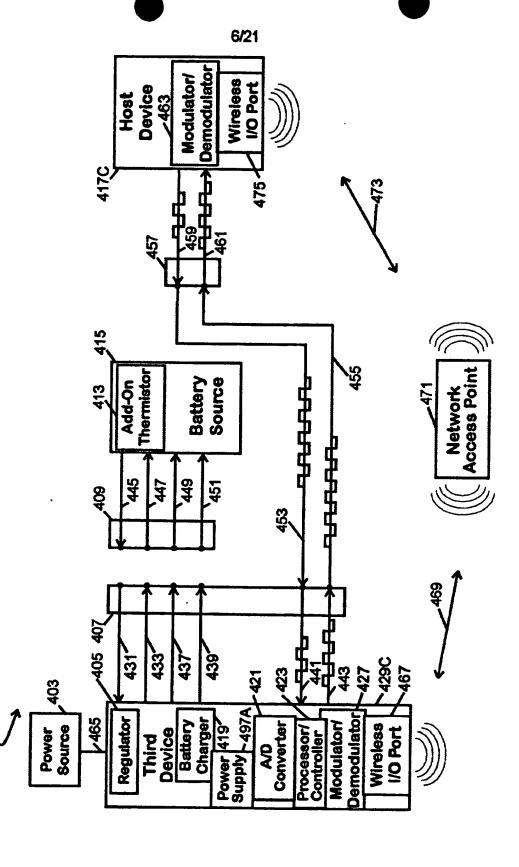
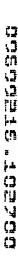


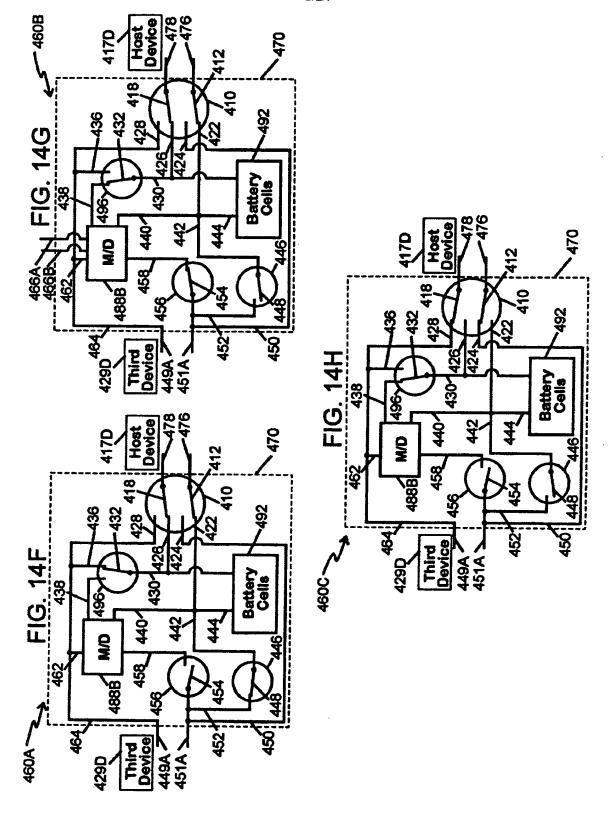
FIG. 14D



Processor Wireless 10 Port Device Host **-463** Memory 475~ <u>&</u> 478 "Smart" Source Circuit Battery Battery Demodulator 415B **Modulator/** #**8** Network Access Point Processor/ Memory Controller Switch § 423B 474B 1024 468B, 492 488A 439A ~ 405 437A. -429D 470A **\_497B** 468A 474A 423A 467 Demodulator Switch A/D Converter Power Source <del>3</del> Modulator Memory Wireless Third Device Regulator Processor/ 10 Par Controller Supply Power

FIG. 14E





Processor Wireless NO Port Memory Device Host 475~ <u>8</u> 415C 476 "Device Emulator" Battery Source Controller Memory "Smart" Processor Battery Switch Circuí Network Access Point Controller Demodulator Controller Processor/ Wireless I/O Port Memory Power Converter Adapter Modulator Switch 423D 488C, (D) 470C, (D) (488C -7 ~497C -423C 429E 474C 465 467 Demodulator **Modulator/** \$84 Wireless Source Power Supply Processor/ Controller Third Device Regulator Power 10 Port Switch Memory

FIG. 141

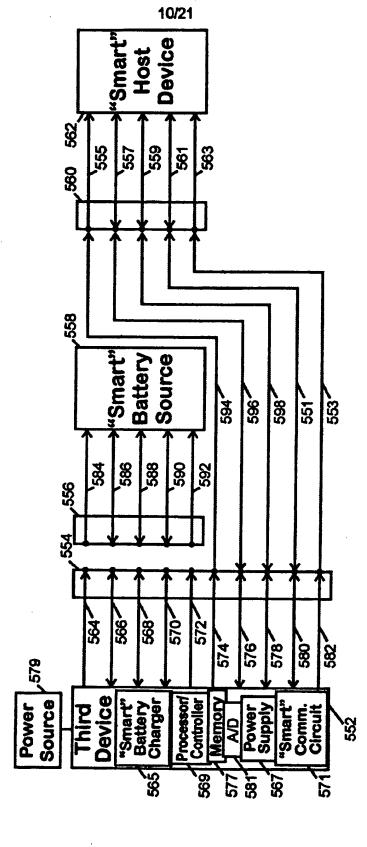
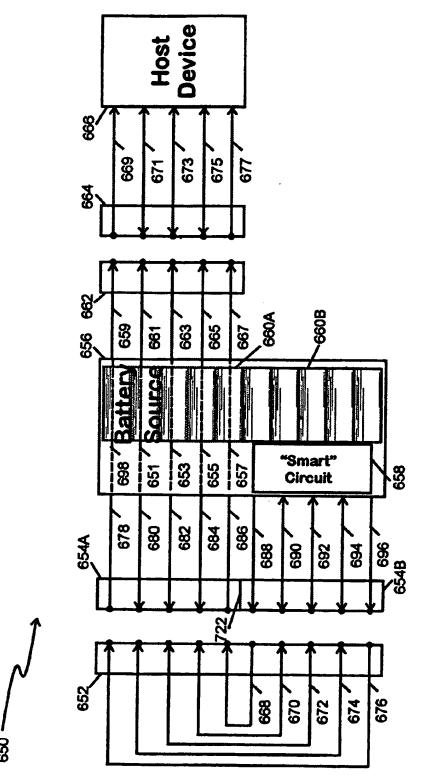
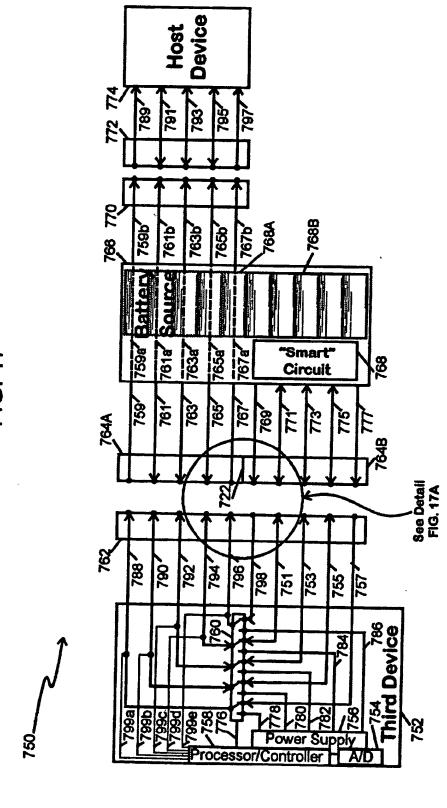


FIG. 15

FIG. 16





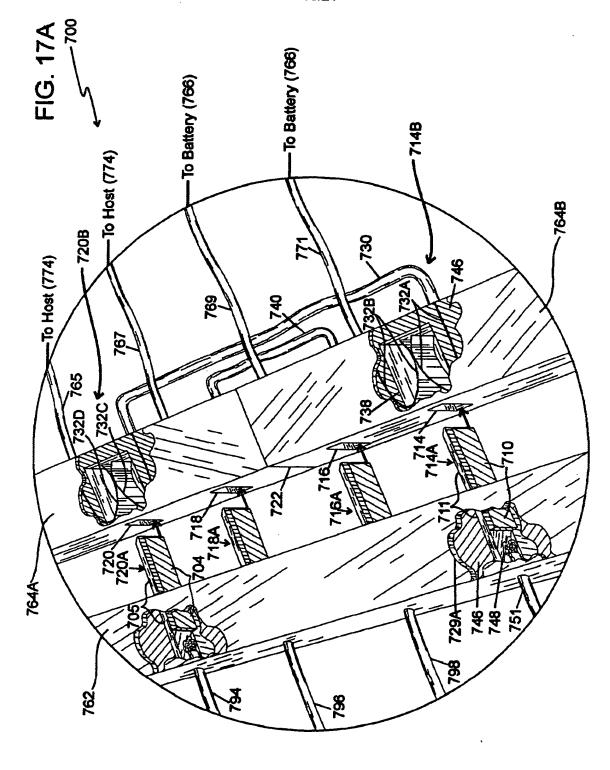


FIG. 17B

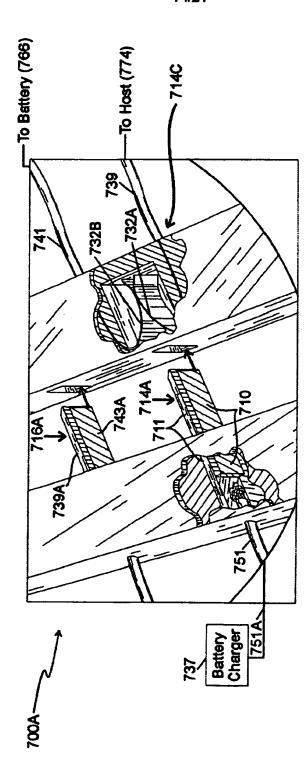


FIG. 17C

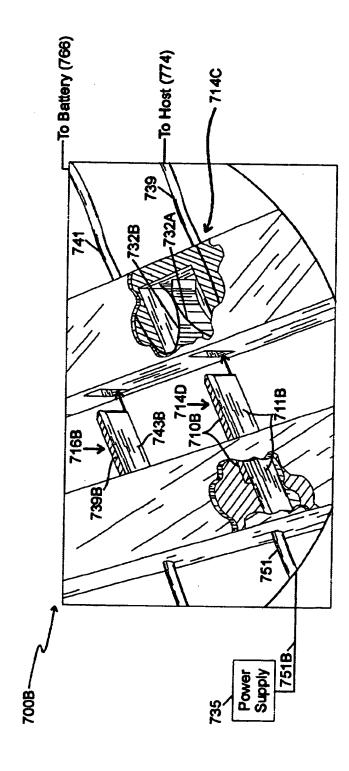


FIG. 18A

